

Abstracts

An MIC Push-Pull FET Amplifier

B.D. Geller and M. Cohn. "An MIC Push-Pull FET Amplifier." 1977 MTT-S International Microwave Symposium Digest 77.1 (1977 [MWSYM]): 187-190.

A push-pull power amplifier, in which the required anti-phase RF voltages result from the use of inherently symmetric and frequency-independent slot line tees, is described. Measurements of the fundamental and harmonic response of this amplifier, which uses an unpackaged, two-cell FET chip and exhibits the well known fourfold impedance advantage over parallel operation, are reported.

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